

Revision 1.00

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Before you begin...

Check the box contents!

The retail motherboard package should contain the following:

	1 x Tiger i7501R S2735 motherboard
The second	2 sets CPU retention modules
	2 x CPU Back-plates with screws
22	1 x ATX to SATA power converter
3	2 x serial ATA cables
\$	1 x 34-Pin floppy drive cable
2	1 x Ultra-DMA-100/66/33 IDE cable
	1 x cable set: 9-pin Serial and 25-pin parallel
	1 x Tiger i7501R S2735 User's Manual
	1 x Tiger i7501R S2735 Quick Reference
	1 x Intel ICH5R SATA RAID driver diskette (Option)
0	1 x TYAN driver CD
Hard Star	1 x I/O shield

If any of these items are missing, please contact your vendor/dealer for replacement before continuing with the installation process.

Chapter 1: Introduction

1.1 - Congratulations!

You have purchased one of the most powerful Dual Intel processor solutions, the Tiger i7501R S2735. Based on Intel's E7501 chipset, the Tiger i7501R S2735 is **Hyper-Threading** ready - utilizing onboard resources so that a second thread of data can be processed in a single processor. Compatible with EPS12V power supplies, this platform offers convenient remote Intelligent Platform Management Interface (IPMI) monitoring through a Server Management Daughter Card. The Tiger i7501R also features an ATX form factor, Gigabit Ethernet port, Fast Ethernet port, and an onboard ATI 8MB PCI RAGE XL VGA, which provides an advanced and versatile solution for your server needs.

Remember to visit TYAN's Website at <u>http://www.tyan.com</u>. There you can find information on all of TYAN's products with FAQs, distributors list and BIOS setting explanations.

1.2 - Hardware Specifications

Processors

- Dual mPG604 ZIF Sockets
- Supports one or two Intel Xeon processors with 512K L2 cache 533MHz, 400MHz FSB and 1MB L3 cache 533MHz FSB
- Onboard 4-phase VRM (VRM 9.1 spec)
- Front-Side Bus support for 533 & 400MHz

Chipset

- Intel E7501 server chipset
- MCH + ICH5R + P64H2 + FWH
- Intel P64H2 supports two PCI-X buses
- Winbond W83627HF Super I/O chip
- Winbond W83782D (or Analog ADM1027***) hardware monitoring chip

***NOTE: ADM1027 chip may be available on some Tiger i7501R models. Check the Tyan website for updates: http://www.tyan.com

Memory

- Six 184-pin 2.5-Volt DDR DIMM sockets
- Supports up to 12GB of Registered
- DDR200/266 (PC1600/PC2100) • Dual channel memory bus
- Supports Registered ECC type memory modules only

Expansion Slots

- Three independent PCI-X/PCI buses
 Two 64-bit 133/100/66MHz (3.3V) PCI-
- X slots • Two 32-bit 33MHz (5V) PCI slots
- Total of four usable slots

Integrated IDE (SATA)

- Provides independent DMA operation on 2 ports*
- Data transfer rates up to 150MB/s
 Supports RAID 0, 1 (with
- 82801ER/ICH5R) (Option)

*NOTE: Maximum number of IDE devices supported with any combination of Integrated IDE and SATA IDE is four devices. For example: 2 SATA and 2 P-ATA

(total=4)

Integrated LAN Controllers

Intel[®] 82546EB dual-channel Gigabit Ethernet controller. (Default configuration.)

- Two RJ-45 connectors with LEDs
- Connected to133MHz PCI-X bus Two 4-pin headers for external LED output
- ** Manufacturing Option:

Intel[®] 82551QM Fast Ethernet controller One RJ-45 LAN connector with LEDs One 4-pin headers for external LED output

Integrated IDE (Parallel ATA)

- Provides two PCI bus master channels for up to four UDMA IDE devices*
- Support for UDMA 33/66/100 IDE and ATAPI devices

Integrated I/O

- One floppy connector supports up to two drives
- Two 9-pin serial ports (one connector and one header)
- One 25-pin ECP/EPP/SPP parallel header
- Four USB 2.0 ports (2 rear connectors and 2 headers)
- PS/2 mouse and keyboard connectors

BIOS

- AMI BIOS 8.0 on 4Mbit Flash ROM
- LAN remote boot (PXE) and SMBIOS v2.3 support
- BIOS Boot Specification v3.1 (BBS) support
- Auto configuration of IDE hard disk types

Form Factor

- ATX footprint (PCB size: 12" x 10.0")
- EPS12V power connector (24-pin and 8-pin)
- One serial connector, & one VGA connector
- Stacked USB (two) connectors and RJ-45 LAN port on top (Optional)
- Stacked PS/2 mouse and keyboard connectors
- Two RJ-45 Side-by-side connectors with LEDs

Hyper-Threading Support In:

Windows 2000 Server Windows 2000 Advanced Server Windows XP Pro Windows NT 4.0 Server + SP6A Windows Server 2003 Linux (Kernel 2.4.18) or higher

TYAN reserves the right to add

support or discontinue support for any OS with or without notice.

Intelligent Platform Management Interface (Manufacturing Option)

- QLogic Zircon Baseboard Management Controller (BMC) based on powerful ARM7 technology
- Tailored for IPMI highest 1.5 specifications
- Supports KCS and BT styles
- Supports flexible Windows and Linux based Management Solution
- Supports RMCP and SNMP protocols
- Supports ASF standard and EMP
- I²C serial multi-master controllers and UARTs
- Supports remote Power on/off and reset support (IPMI-over-LAN)
- Server Management Daughter card connection via a built-in 2x25 header
- SCSI Daughter Card (M7902) via built in SO-DIMM socket

Integrated 2D/3D Graphics

- ATI RAGE XL PCI graphics controller
- 8MB Frame Buffer of video memory

System Management*

- Total of nine 3-pin fan headers
 Six fan headers with tachometer monitoring
- One 2-pin Chassis Intrusion header
- Temperature, voltage and fan
- monitoring
- Watchdog timer

Power Management

ACPI 2.0 compliant

PCI Bus

• PCI rev 2.3 specifications at 33MHz.

Regulatory

- FCC Class B (Declaration of
- Conformity)
- European Community CE (Declaration of Conformity)

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Chapter 2: Board Installation

Installation

You are now ready to install your motherboard. The mounting hole pattern of the Tiger i7501R S2735 matches the ATX specification. Before continuing with installation, confirm that your chassis supports a standard ATX motherboard.

How to install our products right.... the first time!

The first thing you should do is read this user's manual. It contains important information that will make configuration and setup much easier. Here are some precautions you should take when installing your motherboard:

- (1) Ground yourself properly before removing your motherboard from the antistatic bag. Unplug the power from your computer power supply and then touch a safely grounded object to release static charge (i.e. power supply case). For the safest conditions, Tyan recommends wearing a static safety wrist strap.
- (2) Hold the motherboard by its edges and do not touch the bottom of the board, or flex the board in any way.
- (3) Avoid touching the motherboard components, IC chips, connectors, memory modules, and leads.
- (4) Place the motherboard on a grounded antistatic surface or on the antistatic bag that the board was shipped in.
- (5) Inspect the board for damage.

The following pages include details on how to install your motherboard into your chassis, as well as installing the processor, memory, disk drives and cables.

NOTE DO NOT APPLY POWER TO THE BOARD IF IT HAS BEEN DAMAGED

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http://www.TYAN.com	

2.1 – Board Image

The following is an image of the Tiger i7501R S2735.



The above photograph is purely representative. Due to engineering updates and new board revisions, certain components may change and or be repositioned. The picture above may or may not look exactly like the board you received.

The following page will shows the S2735 Block Diagram. You can see the PCI-X/PCI bus assignment.



2.2 – Block Diagram of S2735



The following page includes details on the vital components of this motherboard.



2.3 - Board Parts, Jumpers, and Connectors



This jumper diagram is only a representation of the latest board revision available at the time of publishing. The board you receive may or may not look exactly like the above diagram.



2.4 – Jumper Settings

Jumper	Functio	1	Settings
J16	Force 400MHz FSB instead of	33MHz FSB Header	Page 11
J21	PCI-X A (PCI1) Speed Header		Page 12
J22	10/100 Ethernet LAN1 LED He	der	Page 12
J23	Gigabit Ethernet LAN2 (J15) LI	D Header	Page 12
J24	Gigabit Ethernet LAN3 (J14) LI	D Header	Page 12
J25	Buzzer (System Speaker)		Page 13
J26	SO-DIMM Socket		Page 13
J29	SMDC (System Management I Connector	aughter Card)	Page 13
J31	Hard Disk Activity LED Externa	Input	Page 14
J33	Clear CMOS Jumper		Page 14
J34	Front USB Header		Page 15
J36	COM2 Header		Page 15
J39	Front Panel Connector		Page 16
J43	PCI-X B (PCI2) Speed Header		Page 16

2.5 – Jumper Definitions

Jumper Example

Jumper OFF – open	
Jumper ON – closed	

2.6 – Force 400MHz FSB instead of 533MHz FSB Header (J16)







12 http://www.TYAN.com

2.10 - Buzzer (System Speaker) (J25)



2.11 - SO-DIMM Socket (J26)



2.12 – SMDC (System Management Daughter Card) Connector (J29)



http://www.TYAN.com

2.13 – Hard Disk Activity LED External Input (J31)



2.14 - Clear CMOS Jumper (J33)





2.15 – Front USB Header (J34)

		1 II II II 9 II	2 1 1 10	
AT 2000 X	Signal Description	Pin	Pin	Signal Description
	+5V	1	2	+5V
	Data -	3	4	Data -
	Data +	5	6	Data +
	GND	7	8	GND
	GND	9	10	GND

2.16 - COM2 Header (J36)





2.17 - Front Panel Connector (J39)

Your chassis will usually come with connectors to install onto the motherboard, such as HD and Power LEDs. The Front Panel Connector (J39) has been implemented for such purposes.



2.18 - PCI-X B (PCI2) Speed Header (J43)





2.19 - Fan Header Information

Fan #	Fan Description	Functions	Amp Rated (Maximum)
FAN1	CPU 1 Fan	RPM Read and controlled	1.5A
FAN2	Chassis Fan	RPM Read	1.2A
FAN3	Chassis Fan	RPM Read	1.2A
FAN4	CPU 2 Fan	RPM Read and controlled	1.5A
FAN5	Chassis Fan	RPM Read and controlled	1.2A
FAN6	Chassis Fan	None	2A
FAN7	Chassis Fan	RPM Read	1.2A
FAN8	Chassis Fan	None	2A
FAN9	Chassis Fan	None	2A

FAN connectors



Use these headers to connect cooling fans, both chassis and processor fans, to your motherboard. Cooling fans help keep the system more stable and operating reliably for its product life.

d +12V fans supported





Intel 82870P2 64-bit PCI-X/PCI Controller (P64H2) provides two independent 64-bit 133/100/66MHz PCI-X / PCI bus channels:

First channel:

o Slot (1, 2): 64-bit 133/100/66MHz (3.3V) PCI-X / PCI

3.3 volt or universal PCI-X/PCI cards are accepted

Second channel:

 Onboard Intel's 82546EB 64-bit PCI-X dual gigabit Ethernet controller and SCSI add-on option

- Two RJ45 connectors
- One SO-DIMM connector
- Optional Intel's 82545EM single channel gigabit Ethernet controller

5 volt and universal cards are accepted

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2.21 - Mounting the Motherboard

Before installing your motherboard, make sure your chassis has the necessary motherboard support studs installed. These studs are usually metal and are gold in color. Usually, the chassis manufacturer will pre-install the support studs. If you're unsure of stud placement, simply lay the motherboard inside the chassis and align the screw holes of the motherboard to the studs inside the case. If there are any studs missing, you will know right away since the motherboard will not be able to be securely installed.

Some chassis' include plastic studs instead of metal. Although the plastic studs are usable, Tyan recommends using metal studs with screws that will fasten the motherboard more securely in place.

Below is a chart detailing what the most common motherboard studs look like and how they should be installed.



Mounting the Motherboard

TIP: Use metal studs if possible, as they hold the motherboard into place more securely than plastic standoffs.



2.22 - Installing the Memory

Before attempting to install any memory, make sure that the memory you have is compatible with the motherboard as well as the processor. Critically important is whether you're using the recommended memory for the current board you have. For this information, please check Tyan's web site at: <u>www.tyan.com</u>

The following diagram shows the types of RAM modules you may encounter depending on your board:





Memory Installation Procedure

The Tiger i7501R S2735 requires that memory modules be installed in pairs. Please refer to the next page for exact details on installing memory modules correctly.

When installing memory modules, make sure the modules align properly with the memory socket. There should be keys (small indents) on your memory modules that fit according to the keys in the memory socket. DDR modules and sockets have only one key, which is slightly near the center of the module/socket. The method of installing memory modules is detailed in the following diagrams.



Once the memory modules are firmly seated in the socket, two clamps on either side will close and secure the module into the socket. Sometimes you may need to close the clamps manually.



DDR SDRAM DIMM Slot - 184 Pins (1 key)

To remove the memory module, simply push the clamps outwards until the memory module pops up. Then simply remove the module.

TIP: When installing memory, a module may require a considerable amount of force to seat properly, although this is very rare. To avoid bending and damaging your motherboard, place it on its anti-static bag and onto a flat surface, and then proceed with memory installation.



YOU MUST unplug the power connector to the motherboard before performing system hardware changes, to avoid damaging the board or expansion device.



Memory Pair Installation

When installing DIMM modules, you must install the first two memory modules in **DIMMA_DIMM1 and DIMMB_DIMM1**.

Both modules must be the same in both size and density. Modules must also use the same chip configuration, as in each module that is installed must physically have the same number of memory chips on them.

All memory sockets must be populated by the same memory type, either x4 modules or x8 you cannot mix and match x4 and x8 modules together.



Points to remember:

- At least two Registered ECC DDR SDRAM modules must be installed for the system to turn on and POST (Power On Self Test). Single Channel memory support is not currently available. Please check Tyan's website for updates on this topic.
- When installing modules, install them in this order: DIMMA_DIMM1, DIMMB_DIMM1, DIMMA_DIMM2, DIMMB_DIMM2, etc.
- 128MB, 256MB, 512MB, 1GB, and 2GB Registered ECC PC2100/PC1600 DDR SDRAM memory modules are supported in x4 or x8 configuration.
- The Tiger i7501R S2735 supports up to 12GB of memory with 2GB (X4) registered DDR modules.
- Use **ONLY** x8 **OR** x4 type memory modules. Mixing memory types may affect stability.

RAM chips are organized as x4 bits or x8 bits. On a memory module you're just lining all these chips up in parallel so you have a 64bit wide data bus. All 512MB modules are made using 16 pieces of either 64Mbit x4, or 32Mbit x8. If you use sixteen x8 chips you would get a 128bit data bus, so it has to be split into two separate banks. Each with 64bit bus. That's why 512MB modules based on x8 chips are dual bank. To build 256MB modules you can either use sixteen 32Mx4 chips, or eight 32Mx8 chips. But you can't build a 256MB module using 64Mx4 chips. To build 1GB modules you have to use thirty-two 64Mx4 chips. Two 64Mx4 chips are stacked on top of each other with a lead frame in between. Stacking basically converts two 64Mx4 chips into one 128Mx4 chip or one 64Mx8 chip. Then you stick 16 stacks onto a PCB. Stacking also saves real estate, so you can fit more chips onto a shorter PCB and keep trace lengths in spec.

If you are still unsure whether you are using x4 or x8 memory you can research the memory chip by the SDRAM part numbers, or go to <u>http://www.google.com</u> and search for **jedec 21-c 4.20.4**.

2.23 - Installing the Processor and Heatsink

Your Tiger i7501R S2735 supports the latest processor technologies from Intel. Check the following page on TYAN's website for latest processor support:

http://www.Tyan.com

The following diagrams will detail how to install your processor(s):



The processors you choose to use may not look exactly like the one pictured above, nor will the socket look exactly the same. The diagram is provided as a visual guide to help you install socket processors.

- 1. Lift the lever on the socket until it is approximately 130° or as far back as possible to the socket.
- 2. Align the processor with the socket. There are keys underneath the processor just like on memory modules to ensure that they insert the correct way.
- 3. Seat the processor firmly into the socket by gently pressing down until the processor sits flush with the socket.
- 4. Place the socket lever back down until it snaps into place.
- 5. Your processor is installed.
- 6. Repeat these steps for the second processor if you are using two processors.

NOTE: If using only one CPU, it must be installed in CPU1 socket.

Heatsink Installation

After you are done installing the processor(s), you should proceed to installing their heatsinks. Heatsinks will ensure that the processors do not overheat and continue to operate at maximum performance for as long as you own them. Overheated processors may damage the motherboard.

Because there are many different types of heatsinks available from many different manufacturers, a lot of them have their own method of installation. For the safest method of installation and information on choosing the appropriate heatsink, please refer to Intel's website at http://www.intel.com.

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2.24 - Installing CPU Back-plates and retention modules

The following diagrams will illustrate how to install the included heatsink retention modules and CPU back-plates. You should have: two back-plates and 9 screws. (Four screws for each plate and one spare screw.)

NOTE: The back-plates must be installed prior to installing the motherboard into a chassis. (Certain 1U chassis may not require the back-plates.)

1) Align the heatsink mounting bracket to the holes around the processor socket



Screws should pass through bracket holes and through the motherboard.
 Tighten screws into the CPU back-plates.

NOTE: The hexagonal nut **MUST** be on the viewable side of the CPU backplate as shown below!



- 5) After installing the CPU, seat the heatsink between brackets on processor
- 6) Attach heatsink retaining arms by locking them in the hook as shown below.





Finishing Installing the Heatsink

After you finish installing the heatsink, attach the end wire of the fan (already be attached to the heatsink) to the motherboard.



This diagram illustrates how to connect fans onto the motherboard.

After you're finished installing all the fans you can now connect your drives (hard drives, CD-ROM drives, etc.) to your motherboard.



2.25 – Attaching Drive Cables

Attaching IDE drive cabling is simple. These cables are "keyed" to only allow them to be connected in the correct manner. Tyan motherboards have two on-board IDE channels, each supporting two drives. The black connector designates the Primary channel, while the white connector designates the Secondary channel.

Attaching IDE cables to the IDE connectors is illustrated below:



Simply plug in the **BLUE END of the IDE cable** into the motherboard IDE connector, and the other end(s) into the drive(s). Each standard IDE cable has three connectors, two of which are closer together. The BLUE connector that is furthest away from the other two is the end that connects to the motherboard. The other two connectors are used to connect to drives.





Floppy Drives

Attaching a floppy drive can be done in a similar manner to an IDE drive. See the diagram below for an example of a floppy cable. Most of the current floppy drives on the market require that the cable be installed with the colored stripe positioned next to the power connector. In most cases, there will be a key pin on the cable which will force proper connection of the cable.



The first floppy drive (commonly denoted as A:) is usually attached to the end of the cable with the twist in it. Drive B: is usually connected to the second or third connector in the cable (the second or third connector after you install Drive A:).

Refer to your floppy drive's installation instructions (if available), or contact your dealer if you are unsure about how to attach the floppy drive(s). Remember, you can only have 2 floppy drives connected at any given time.

Below are some symptoms of incorrectly installed floppy drives. While they are minor and installing them incorrectly doesn't cause severe problems, it may cause your system to freeze or crash when trying to read and/or write to diskettes.

Symptoms of incorrectly installed floppy drives					
Drive is not aut matically detected	Usually caused by faulty cables, cables put in backwards or a bad floppy drive or motherboard. Try another floppy drive to verify the problem if the cable is properly installed or try replacing the actual cable. Also check to see if the onboard floppy controller is enabled in the BIOS setup.				
Drive Fail message at bootup	The cable, floppy drive or motherboard may be faulty. Try another drive or cable to verify.				
Drive doe: not power on	Check power cable and cabling. Maybe a bad power supply or drive cable problem.				
Drive activity light is constantly on	Usually signifies that the cable on the drive is on backwards, which is a common issue. Reverse the cable at the floppy drive end and try again.				

2.26 - Installing Add-In Cards

Before installing add-in cards, it's helpful to know if they are fully compatible with your motherboard. For this reason, we've provided the diagrams below, showing the most common slots that may appear on your motherboard. Not all of the slots shown will necessarily appear on your motherboard, however, there will be combinations of what you see here.



Simply find the appropriate slot for your add-in card and insert the card firmly. Do not force any add-in cards (or anything else) into any slots if they won't seat in place. It's better to try another slot or return the faulty card rather than damaging both the motherboard and the add-in card.

TIP: It's good practice to install add-in cards in a staggered manner, rather than directly adjacent to each other. This allows air to more easily circulate within the chassis, providing improved cooling for all installed devices.



YOU MUST unplug the power connector to the motherboard before performing system hardware changes, to avoid damaging the board or expansion device.



2.27 – Connecting External Devices

Connecting external devices to the motherboard is an easy task. The standard devices you should expect to plug into the motherboard are keyboards, mice, and printer cables. The following diagram will detail the rear port stack for this S2735 motherboard:



Onboard LAN LED Color Definition

Left

Left Right

	10/100 Mbps LAN Link/Activity LED Scheme			
	Speed	Left LED	Right LED	
Right	Link 10Mbps	Off	Off	
	Activity 10Mbps	Blink Green	Off	
	Link 100Mbps	Off	Yellow	
	Activity 100Mbps	Blink Green	Blink Yellow	

10/10	10/100/1000 Mbps LAN Link/Activity LED Scheme				
Speed	Left LED	Right LED			
Link 10Mbps	Green	Off			
Activity 10Mbps	Blink Green	Off			
Link 100Mbps	Off	Yellow			
Activity 100Mbps	Off	Blink Yellow			
Link 1000Mbps	Green	Yellow			
Activity 1000Mbps	Blink Green	Blink Yellow			



2.28 - Installing the Power Supply

There are two power connectors on your Tiger i7501R S2735. The Tiger i7501R S2735 requires that you have an EPS12V power supply that has a 24-pin and an 8-pin power connector. Please be aware that ATX 2.x, ATX12V and ATXGES power supplies are not compatible with the board and can damage the motherboard and/or CPU(s).

EPS12V



EPS12V (8-pin) Power Connector

Disconnect power supply from electrical outlet

- 1. Connect the EPS12V 8-pin power connector
- 2. Connect the EPS12V 24-pin power connector
- 3. Connect power cable to power supply to power outlet

Make sure you have connected both connectors before attempting to apply power to the board.



YOU MUST unplug the power supply before plugging the power cables to motherboard connectors.

2.29 – Finishing Up

Congratulations on making it this far! You're finished setting up the hardware aspect of your computer. Before closing up your chassis, make sure that all cables and wires are connected properly, especially IDE cables and most importantly, jumpers. You may have difficulty powering on your system if the motherboard jumpers are not set correctly.

In the rare circumstance that you have experienced difficulty, you can find help by asking your vendor for assistance. If they are not available for assistance, please find setup information and documentation online at our website or by **calling your vendor's support line.**

Chapter 3: BIOS

3.0 - BIOS Setup Utility

With the BIOS setup utility, you can modify BIOS settings and control the special features of your computer. The setup utility uses a number of menus for making changes and turning the special features on or off.



All menus are based on a typical system. The actual menus displayed on your screen may be different and depend on the hardware and features installed in your computer.

To start the BIOS setup utility:

a. Turn on or reboot your system

 Press during POST (F4 on remote console) to start BIOS setup utility

BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
System Ove	erview	Use [ENTER], [TAB]				
AMIBIOS Version : 08 00 09					TAB] to d	
Build Date : ID :	xx/xx/2003 0ABBP006				Use [+] or [configure s	-] to ystem time.
Processor Type : Logical Cou	Processor Type : Intel(R) Xeon (TM) CPU x.xx GHz Logical Count : x					
System Mer Size :	nory xxxx MB				$\leftarrow \rightarrow \text{Select}$ $\uparrow \downarrow \text{Select}$ $+/- \text{Chan}$ E1 Gene	t Screen tem ge Option
System Tim System Date	e e	[12:59:59] [xxx xx/xx	 /2003]		F10 Save ESC Exit	and Exit

To select an item

Use the left/right ($\leftarrow \rightarrow$) arrow keys to make a selection

To display a sub-menu (A pointer " ▶ " marks all sub menus)

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>.

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 T) (A A I	

3.1 – BIOS Menu Bar

The menu bar at the top of the windows lists these selections:

Main	To configure basic system setups
Advanced	To configure the advanced chipset features
PCI/PnP	To configure legacy Plug & Play or PCI settings
Boot	To configure system boot order
Security	To configure user and supervisor passwords
Chipset	To configure chipset management features
Power	To configure power management features
Exit	To exit setup utility

NOTE

Options written in **bold type** represent the BIOS setup default

3.2 – BIOS Legend Bar

The chart describes the legend keys and their alternates:

Key	Functior			
<f1> or <alt-h></alt-h></f1>	General help window			
<esc></esc>	Exit current menu			
$\leftarrow \rightarrow$ arrow keys	Select a different menu			
\uparrow or \downarrow arrow keys	Move cursor up/down			
<tab> or <shift-tab></shift-tab></tab>	Cycle cursor up/down			
<home> or <end></end></home>	Move cursor to top/bottom of the window			
<pgup> or <pgdn></pgdn></pgup>	Move cursor to next/previous page			
<f5> or <-></f5>	Select the previous value/setting of the field			
<f6> or <+> or <space></space></f6>	Select the next value/setting of the field			
<f8></f8>	Load Fail Safe default configuration values of the menu			
<f9></f9>	Load the Optimal default configuration values of the			
	menu			
<f10></f10>	Save and exit			
<enter></enter>	Execute command or select submenu			

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3.3 – BIOS Main Menu

The Main BIOS Menu is the first screen that you can navigate. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. "Grayed-out" options cannot be configured, options in blue can be changed.

The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often, a text message will accompany it.

BIOS Setup Utility					
Main	Advanced	PCI/PnP	Boot	Security	Chipset Exit
System Overview				Use [ENTER], [TAB] or	
AMIBIOS Version : (Build Date : ; ID : (08.00.09 xx/xx/2003 0ABBP006				[SHIFT_TAB] to select a field Use [+] or [-] to configure system time.
Processor Type : Intel (R) Xeon (TM) CPU x.xx GHz Logical Count : x					
System Mem Size	nory xxxx MB				← → Select Screen $\uparrow \downarrow$ Select Item Enter Go to Sub Screen
System Time System Date	9	[12:59:59] [xxx xx/xx/2	003]		F1 General Help F10 Save and Exit ESC Exit

Feature	Option	Description	
Main			
System Time	HH : MM : SS	Set the system time	
System Date	MM : DD : YYYY	Set the system date	



3.4 – BIOS Advanced Menu

You can select any of the items in the left frame of the screen, such as Super I/O Configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screen is shown below. The sub menus are described on the following pages.

BIOS Setup Utility				
Main Advanced	PCI/PnP	Boot	Securit	y Chipset Exit
Advanced Settings	Use [ENTER], [TAB] or			
WARING: Setting wrong va cause system t	alues in belo o malfunctio	w sectior n.	ns may	[SHIFT_TAB] to select a field
 CPU Configuration IDE Configuration Floppy Configuration Super I/O Configuration ACPI Configuration DMI Event Logging Remote Access Configu USB Configuration Onboard Devices Config Hardware Monitor 	ration guration			Use [+] or [-] to configure system time. ← → Select Screen ↑ ↓ Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC. Exit

Feature	Option	Description	
Advanced Settings			
CPU Configuration	Menu Item	Configure CPU	
IDE Configuration	Menu Item	Configure the IDE device(s)	
Floppy Configuration	Menu Item	Configures devices connected to the floppy controller	
Super I/O Configuration	Menu Item	Configures devices connected to the Super I/O Configuration	
ACPI Configuration	Menu Item	Section for Advanced ACPI Configuration	
DMI Event Logging	Menu Item	Views & controls Event Log	
Remote Access Configuration	Menu Item	Configures Console Redirect	
USB Configuration	Menu Item	Configures USB controller & legacy device support	
Onboard Devices Configuration	Menu Item	Use this section to Enable / Disable special Onboard Devices	
Hardware Monitor	Menu Item	Hardware Monitor & Dis	
3.4.1 – CPU Configuration Sub-Menu You can use this screen to select options for the CPU Configuration Settings. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option.

BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Śecurity	Chipset	Exit
Configure a	advanced CPL	J settings			Use [ENTER], [TAB]	
Manufacture	er: Intel				or [SHIFT_] select a field	ГАВ] to d
Brand String] : Intel (R) Xe	eon (TM) CF	PU x.xxG	Hz		1 to
Cache L2 Cache L3	: 512 KB : 0 KB				configure sy	/stem time.
Ratio Status Ratio Actua Ratio CMOS	3 : Unlocked value : 15 3 Setting:			[255]	↑ ↓ Select + - Chang F1 Genet	t Item ge Option ral Help
Hyper Threa	ading			[Enabled]	ESC Exit	and Exit

Feature	Option	Description
Configure advanced CPU se	ttings	
Rati⊢ Status	Unlocked	
Ratio Actual value	xx	
Ratio CMOS Setting	255	Sets the ratio between CPU Core Clock and the FSB Frequency. NOTE: If an invalid ratio is set in CMOS then actual and set point values may differ.
Hyper Threading	Enabled	ENABLE: Enable CPU HyperThreading for HT enabled processor(s).
	Disabled	DISABLE: Disable CPU HyperThreading for HT enabled processor(s).



3.4.2 – IDE Configuration Sub-Menu You can use this screen to select options for the IDE Configuration Settings. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option.

BIOS Setup Utility					
Main Advanced	PCI/PnP	Boot	Security	Chipset	Exit
IDE Configuration	Use [ENTER], [TAB]				
IDE Legacy Configuration	[P-AT	A Only]	or [SHIFT_TAB] to select a field		
S-ATA Running Enhance P-ATA Channel Selec S-ATA Ports Definitior Configure S-ATA as R	ed Mode tion 1 AID	[Yes] [Both] [P0-3r [No]	d./P1-4th]	Use [+] or configure	[-] to system time.
 IDE channel 0 Master IDE channel 0 Slave IDE channel 1 Master IDE channel 1 Slave IDE channel 2 Master IDE channel 2 Slave IDE channel 3 Master IDE channel 3 Slave Hard Disk Write Protect IDE Detect Time Out (Sec) ATA (PI) 80pin Cable Detect 	oction	: [› : [› : [› : [› : [› : [› : [› : [›	xxx] xxxx] xxxx] xxxx] xxxx] xxxx] xxxx] ble] t & Device]	← → Sele ↑ ↓ Seleo +/- Cha F1 Gen F10 Save ESC Exit	ct Screen ct Item nge Option eral Help e and Exit

Feature	Option	Description
IDE Configuration		
	Disabled	Select combination you wish to
	P-ATA only	Use Note only 4 IDE drives can
IDE egacy Configuration	S-ATA only	be used at one time no matter
	P-ATA and S- ATA	what the combination.
S-A ⁻ A Running Enhanced	Yes	
Mod	No	
	Primary	
P-A ⁻ A Channel Selection	Secondary	
	Both	
$S_A^- A$ Ports Definition	P0-3rd./P1-4th	
S-A TORS Deminion	P0-4th./P1-3rd.	
Con Jure S-ATA as RAID	No	
Con guie S-ATA as RAID	Yes	

Feature	Option	Description	
IDE Configuration			
SAT port 0/1 Master	Both	While entering setup, BIOS	
of the point of this detail	Primary	IDE devices. This displays the	
SAT \ port 0/1 Slave	Secondary	status of auto detection of IDE	
	Disabled	devices.	
	Auto	Auto - To determine the IDE	
Primary/Secondary Master	User	User - To set IDE drive type by	
Primary/Secondary Slave	ATAPI Removable	user ATAPI Removable – Read/write	
Fillind y/Secondary Slave	CD-ROM	media (e.g. IDE ZIP)	
	None	drive	
Hard Dick Write Protect	Disabled	This option protects the first	
	Enabled	being written.	
	0		
	5		
	10	Configure the time (in Seconds)	
IDE Detect Time Out (Sec)	15	before the BIOS times out on	
,	20	detecting an IDE Device.	
	25	Ĵ	
	30		
	35		
ATA (PI) 80pin Cable	Host & Device	Togales the detection of 80wire	
Detection	Host	IDE cables.	
	Device		

3.4.2.1 – SATA Port / Primary IDE / Secondary IDE Configuration Sub-Menu You can use this screen to select options for the SATA Port / Primary IDE / Secondary IDE Configuration Settings. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option.

BIOS Setup Utility					
Main Advanced	PCI/PnP	Boot	Security	Chipset	Exit
SATA Port 0 IDE Master				Use [ENTE	R], [TAB]
Device:	[P-ATA Only]		or [SHIFT_TAB] to select a field		
Type LBA/Large Mode Black (Multi – Sector Transfer) PIO Mode S.M.A.R.T. 32Bit Data Transfer		[Auto [Auto [Auto [Auto [Auto [Disal]]]]] oled]	Use [+] or [- configure sy ← → Select ↑ ↓ Select +/- Chan F1 Gene F10 Save ESC Exit	-] to ystem time. t Screen Item ge Option ral Help and Exit

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3.4.3 – Floppy Configuration Sub-Menu

You can use this screen to specify options for the Floppy Configuration Settings. Use the up and down <Arrow> keys to select an item. Use the <Plus> and <Minus> keys to change the value of the selected option. The settings are described on the following pages.

	BIOS Setup Utility					
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Floppy Configuration					Use [ENTER], [TAB]	
					or [SHIFT_ select a fiel	TAB] to d
Floppy A Floppy B		[1.44 MB 3 1/2"] [Disabled]			Use [+] or [configure s	-] to ystem time.
				$\leftarrow \rightarrow$ Select $\uparrow \downarrow$ Select +/- Chan F1 Gene F10 Save ESC Exit	t Screen : Item ge Option ral Help and Exit	

Feature	Option	Description		
Floppy Configuration				
	Disabled	This setting selects the type of		
	360 KB 51/4"	the floppy disk drive installed		
Floor (A	1.2 MB 51/4"	system.		
	720 KB 31/2"			
	1.44 MB 31/2"			
	2.88 MB 31/2"			
	Disabled	This setting selects the type of		
	360 KB 51/4"	the floppy disk drive installed in		
Floor / B	1.2 MB 51/4"	system.		
Liobt / P	720 KB 31/2"			
	1.44 MB 31/2"]		
	2.88 MB 31/2"			

3.4.4 – Super I/O Configuration Sub-Menu You can use this screen to select options for the Super I/O settings. Use the up and down arrow (\uparrow/\downarrow) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option

BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Win627 Super I/O Chipset				Use [ENTER], [TAB] or [SHIFT_TAB] to		
Onboard Flo Serial Port1 Serial Port2 Serial Port Parallel Port Parallel Port	ppy Controller Address Address t2 Mode Address Mode IRQ		[En [3F [Di: [Di: [No [IR	abled] 8/IRQ4] sabled] sabled] mal] Q7]	Use [+] or configure s ← → Selec ↑ ↓ Selec +/- Char F1 Gene F10 Save ESC Exit	[-] to system time. t Screen t Item ige Option eral Help and Exit

Feature	Option	escription	
Win627 Super I/O Chipset			
Onboard Floppy Controller	Enabled	Enables or Disables the Onboard	
Chibbard hoppy Controller	Disabled	Floppy Controller.	
	3F8/IRQ4	Cata the earliel next 4 (COM4) have	
Serial Port1 Address	3E8/IRQ4	Sets the senal port 1 (COMT) base	
Senari orti Address	2E8/IRQ3	Disabled -turn off port	
	Disabled		
	2F8/IRQ3	Sate the partial part 2 (COM2) have	
Serial Port2 Address	3E8/IRQ4	VO address and an interrupt number	
Senari onz Address	2E8/IRQ3	Disabled –turn off port	
	Disabled		
	Normal	Allows BIOS to Select Mode for	
Serial Port2 Mode	IrDA	Serial Port2	
	ASK IR	Schult Stiz.	
	378	Assigns the Barallel Bart hass I/O	
Parallel Port Address	278	address	
	3BC	Disabled –turn off port	
	Disabled		
	Bi-Directional	Configures Parallel port mode.	
Devellet Devit Maria	Normal	Bi-Directional= send & receive data	
Parallel Port Mode	EPP	Normal= can send data	
	ECP	ECP=Extended Capability port	
Devellet Devet liste menut	IRQ 7		
Parallel Port Interrupt	IRQ 5	Assigns IRQ to parallel port.	

3.4.5 – ACPI Settings Sub-Menu Use this screen to select options for ACPI. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. A description of the selected item appears on the right side of the screen. The settings are described on this page. The screen is shown below.

BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
ACPI Setting	gs				Use [ENTE	R], [TAB] or
ACPI Aware	O/S		[Yes]		[SHIFT_TAI	3] to select a
 Advanced 	ACPI Configura	ation			Use [+] or [- system time] to configure
WOL S48S5	Support		[Enab	oled]	$\leftarrow \rightarrow$ Select	Screen
Power Button Instant Off		[Enabled]		↑↓ Select	Item	
After Power	Failure		[Alwa	ys Off]	F1 Gener F10 Save ESC Exit	and Exit

Feature	Option	escription	
ACPI Settings			
ACPLAware O/S	Yes	Yes allows the system to utilize	
Nor Friware 0/0	No	Power Interface) specification.	
WOL \$48\$5 Support	Enabled	Enable WOL event before system	
WOL 34833 Support	Disabled	goes to S4855 mode	
Power Button Instant Off	Enabled	The Power Button Instant-off	
r ower Button motant on	Disabled	O.S.	
	Always On	Determines the mode of operation if	
After Power Failure	Always Off	an AC nower failure occurs	
	Previous state		

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3.4.5.1 – Advanced ACPI Configuration Sub-Menu

Use this screen to select options for the ACPI Advanced Configuration Settings. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. A description of the selected item appears on the right side of the screen. The settings are described on this page. The screen is shown below.

Main Advanced	BIOS PCI/PnP	Setup U Boot	tility Sec	urity	Chipset	Exit	
Advanced ACPI Configuration					Use [ENTER]. [TAB] or		
ACPI 2.0 Support ACPI APIC Support AmI OEMB table Headless mode	[No] [Enab [Disab	led] led] bled]		[SHI field Use syste ↑↓ +/- F1 F10 ESC	[+] or [-] to c em time. Select Scre Select Item Change Op General He Save and E Exit	onfigure een elp Exit	

Feature	Option	Description	
Advanced ACPI Configurati	on		
ACDI 2.0 Support	Yes	Set this value to allow or prevent	
	No	the ACPI 2.0 specification.	
	Enabled	This option allows you to define	
ACETAFIC Support	Disabled	management features.	
Aml OEMB table	Enabled	Set this value to allow the ACPI BIOS to add a pointer to an OEMB table in the Root System Description Table (RSDT) table.	
	Disabled	Note: OEMB table is used to pass POST data to the AML code during ACPI O/S operations.	
Hoodloss modo	Enabled	Enable / Disable Headless	
	Disabled	operation mode through ACPI.	

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3.4.6 – DMI Event Logging Sub-Menu

You can use this screen to view the Event Log Control Menu. This logs system events (such as CMOS clear, ECC memory errors, etc) and writes the log into NVRAM. Use the up and down arrow (\wedge/ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. The settings are described on the following pages.

BIOS Setup Utility					
Exit					
ER], [TAB] or					
AB] to select					
[-] to system time.					
ct Screen xt Item nge Option					
o Sub Screen eral Help erand Exit					
ct st I ng o S era era					

Feature	Option	Description
DMI Event Logging		
View Event Log		View all unread events on the Event Log.
Mark All Events as Read		Mark all unread events as read.
Clear Event Log		Discard all events in the Event Log.
Event Log Statistics		Displays the storage capacity & usage of the Event Log.

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3.4.7 – Remote Access Configuration Sub-Menu

You can use this screen to view the Remote Access Configuration Menu. This feature allows access to the Server remotely via serial port. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. The settings are described on the following pages.

		BIOS	Setup U	tility		
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Configure	Remote Acces	Use [ENTER], [TAB]				
Remote Acc	cess	[Disabled]			or [SHIFT_ select a fiel	TAB] to d
					Use [+] or [- configure sy	-] to ystem time.
				← → Select ↑ ↓ Select +/- Chan F1 Gene F10 Save ESC Exit	t Screen Item ge Field ral Help and Exit	

Feature	Option	Description			
Configure Remote Access type and parameters					
	Disabled	Enables remote access to			
Remote Access	Serial	system through serial port.			
	SMDC				

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3.4.8 – USB Configuration Sub-Menu

You can use this screen to view the USB Configuration Menu. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. The settings are described on the following pages.

		BIOS	Setup	Utility		
Main A	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
USB Configur	ration				Use [ENTE	R], [TAB]
Module Versio	n – X.XX.X->	K.X			or [SHIFT_ select a fie	TAB] to ld
USB Devices E	Enabled:					
	None				Use [+] or configure s	-] to system time.
USB Function Legacy USB S USB 2.0 Contr USB 2.0 Contr • USB Mass S	support oller oller Mode Storage Devid	ce Configura	ן [מtion	4 USB Ports] Enabled] Enabled] [HiSpeed]	← → Selec ↑ ↓ Selec +/- Char F1 Gene F10 Save ESC Exit	ct Screen t Item age Option eral Help and Exit

Feature	Option	Description		
USB Configuration				
	Disabled			
USB Function	2 USB ports	Enables USB host controllers.		
	4 USB ports			
	Auto	Enables support for legacy USB		
Legacy USB Support	Disabled	devices such as keyboards,		
	Enabled	mice, & bootable USB devices.		
USB 2.0 Controller	Disabled	Select "Enabled" if your system		
	Enabled	you have a USB Keyboard.		
	FullSpeed	Configures the USB 2.0		
USB 2.0 Controller Mode	HiSpeeed	(480Mbps) or FullSpeed (12Mbps)		

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3.4.8.1 – USB Mass Storage Device Configuration Sub-Menu

You can use this screen to view the USB Mass Storage Device Configuration Menu. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. The settings are described on the following pages.

Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
USB Mass	Storage Devic	e Configura	ation		Use [ENTER], [TAB]	
USB Mass	Storage Reset	[20 Sec]	or [SHIFT_TAB] to select a field	TAB] to d		
No USB Mass Storage device detected				Use [+] or [-] to configure system time.		
					$\leftarrow \rightarrow$ Select $\uparrow \downarrow$ Select +/- Chan F1 Gene F10 Save ESC Exit	t Screen t Item ge Option ral Help and Exit

Feature	Option	Description
USB Configuration		
	10 Sec	Number of seconds POST
USB Mass Storage Reset Delay	20 Sec	waits for the USB mass storage
	30 Sec	device after start unit
	40 Sec	command.

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3.4.9 – Onboard Devices Configuration Sub-Menu

Use this screen to select options for the Onboard Devices Configuration Settings. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. A description of the selected item appears on the right side of the screen. The settings are described on this page. The screen is shown below.

BIOS Setup Utility						
Boot	Security	Chipset	Exit			
<u>ration setting</u> [Enabled] [Enabled] [Enabled] [Enabled] [Enabled]	Security IS Use [[SHIF field Use [syste ← → +/- F1 F10 F10 F10	Chipset ENTER], [T/ T_TAB] to s (+) or [-] to co m time. Select Scre Select Scre Select Item Change Op General He Save and E	Exit AB] or select a onfigure en tion Ip xit			
	OS Setup Util Boot ation setting [Enabled] [Enabled] [Enabled] [Enabled] [Enabled]	DS Setup Utility P Boot Security ation settings [Enabled] [En	DS Setup Utility Description Chipset ation settings Use [ENTER], [T/] [Enabled] [SHIFT_TAB] to strict [Enabled] Use [+] or [-] to car [Enabled] Charge Op [Enabled] F1 General He F10 Save and E ESC			

Feature	Option	Description				
Special onboard devices configuration settings						
Ophoard ATLVidoo	Enabled	Toggles the onboard ATI video				
Onboard ATT Video	Disabled	graphics.				
Onboard 82551 LAN	Enabled	Togales 10/100Mbit LAN port				
Official 02551 LAN	Disabled	Toggles To Toombit LAN port.				
Remote Boot OS by 82551	Enabled	Remote boot OS from 82551				
Remote Boot OS by 02001	Disabled					
Onboard 82546 Dualport	Enabled	Toggles the Gigabit I AN ports				
LAN	Disabled	Toggles the Gigabit EAN ports.				
Remote Boot OS by 82546	Enabled	Remote boot OS from 82546				
	Disabled					

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3.4.10 – Hardware Monitor Sub-Menu You can use this screen to view the Hardware Monitor Configuration Settings. Use the up and down arrow (\uparrow/\downarrow) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. The settings are described on the following pages.

	BIOS Setup Utility							
Main 🖌	Advanced	PCI/PnP	Boot	Security	Chipset	Exit		
Monitor CPU te	Use [ENTEF	R], [TAB] or						
CPU Temperature					[SHIFT_TAB] to select a field			
CPU2			:xx (Use [+] or [-] configure sy] to stem time.		
Fan Speed								
CPU1 Fan			:XXX	x RPM				
CPU2 Fan			:XXX	x RPM				
Fan 2			:XXX	x RPM				
Fan 3			:XXX	x RPM	$\leftarrow \rightarrow \text{Select}$	Screen		
Fan 5			:XXX	x RPM	↑ ↓ Select	Item		
Fan 7			:xxx	x RPM	+/- Chang	e Option		
Voltage			:x.x)	αV	Tab Select F1 Gener	Field al Help		
+3.3V			:x.x)	αV	F10 Save a	and Exit		
+5V +12V			:xx.)	(XX V	ESC Exit			
Chassis Intrusic	on Detect		[Dis	abled]				

Feature	Option	Description			
Monitor CPU temperature, Fan speed and Voltage					
CPU1 Temperature		Displays CPU Temperatures			
CPU2 Temperature					
CPU1 Fan					
CPU2 Fan		Displays speed of fans connected to appropriate Fan headers.			
Fan 2					
Fan 3					
Fan 5					
Fan 7					
+3.3V		Displays Voltage for CPU			
+5V		memory & other devices			
+12V		memory, a other devices.			
Chassis Intrusion Datast	Disabled	Enabled / Disabled: when			
Chassis intrusion Detect	Enabled	BIOS will record the event.			

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3.5 – Advanced PCI/PnP Menu

You can use this screen to view PnP (Plug & Play) BIOS Configuration Menu. This menu allows the user to configure how the BIOS assigns resources & resolves conflicts. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option. The settings are described on the following pages.

		tility				
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Advanced I	PCI/PnP Setti		Use [ENTER], [TAB] or			
WARING: S cause syste	etting wrong v m to malfunct	field				
Plug & Play OS PCI Latency Timer Allocate IRQ to PCI VGA Palette Snooping PCI IDE BusMaster			() 66] [Y [E]	'es] 4] es] Disabled] Inabled]	Use [+] or [-] system time.	to configure
IRQ3 IRQ4 IRQ5 IRQ7 IRQ9 IRQ10 IRQ11 IRQ14 IRQ15			A] A] A] A] A] A]	.vailable] .vailable] .vailable] .vailable] .vailable] .vailable] .vailable] .vailable] .vailable]		
DMA Chann DMA Chann DMA Chann DMA Chann DMA Chann DMA Chann Reserved M	iel_0 iel_1 iel_3 iel_5 iel_6 iel_7 lemory Size		A] A] A] A] A] []	vailable] vailable] vailable] vailable] vailable] vailable] vailable]	← → Select i ↑ ↓ Select l +/- Changu E1 Generc	Screen tem e Option
Extended B	IOS Data Area	a	(E	nabled]	F10 Save a ESC Exit	nd Exit

Feature	Option	Description		
Advanced PCI/PnP Settings				
	Yes	The Yes setting allows the operating system to change the interrupt, I/O, and DMA settings. Set this option if the system is running Plug and Play aware		
Plug & Play US	No	operating systems. Set No for operating systems that do not meet the Plug and Play specifications. It allows the BIOS to configure all the devices in the system.		
	32	This setting controls how many		
	64	PCI clocks each PCI device can		
	96	hold the bus before another PCI		
PCI Latency Timer	128	device takes over. When set to		
	160	higher values, every PCI device		
	192	can conduct transactions for a longer time and thus improve the effective PCI bandwidth.		
	224			
	248			
Allocate IRQ to PCI VGA	Yes	Allows or restricts the system from		
	No	giving the VGA adapter an IRQ.		
Palette Snooping	Disabled	This is the default setting and should not be changed unless the		
	Enabled	Palette Snooping to be Enabled.		
PCI IDE Bus Master	Disabled	ENABLED: BIOS uses PCI bus mastering for reading / writing to		
	Enabled	IDE drives.		
IRO3 ~ IRO15	Available	Allows user to reserve a specific IRQ for a legacy device (Note:		
	Reserved	most hardware devices & OS used do not support manual assigned).		
DMA0 ~ 7	Available	Allows user to reserve a specific		
	Reserved	DMA for a legacy device.		
	Disabled			
Reserved Memory Size	16 K	Size of memory block to reserve		
	32 K	for legacy ISA devices.		
	64 K	<u> </u>		
Extended BIOS Data Area	Enabled	Enable / Disable Extended BIOS		
	Disabled	Data Area.		

3.6 - BIOS Boot Settings Menu

You can display Boot Setup option by highlighting it using the Arrow (\uparrow/\downarrow) keys and pressing Enter. The settings are described on the following pages.

BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Boot Settin	gs	Use [ENTER], [TAB] or [SHIFT_TAB] to select a field				
 Boot Sett 	tings Configur					
 Boot Dev Hard Disl Removable 	 Boot Device Priority Hard Disk Drives Removable Drives 					to stem time.
ATAPI CI	ATAPI CDROM Drives					Screen
					↑ ↓ Select I	tem
					F1 Genera	al Help
					F10 Save a ESC Exit	ind Exit

3.6.1 – Boot Settings Configuration Sub-Menu Use this screen to select options for the Boot Settings Configuration. Use the up and down arrow (\uparrow/\downarrow) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option.

BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Boot Settin	gs Configurat	Use [ENTER], [TAB] or				
Quick Boot Quiet Boot Add On ROI	M Display Mod	sabled] sabled] rce BIOS]	[SHIFT_TAB] to select a field			
Boot up Number-Lock PS/2 Mouse Support			[Or [Au	i] ito]	Use [+] or [-] to configure system time.	
Hit 'DEL' Message Display			[En	abled]	← → Select	Screen
Interrupt 19 Capture Primary Display Device		[Au	ito]	 ↑ ↓ Select Item +/- Change Option F1 General Help F10 Save and Exit ESC Exit 		

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Feature	Option	Description		
Boot Settings Configuration	on			
Quick Boot Mode	Enabled	This option allows user bypass BIOS		
Quick Doot Mode	Disabled	self test during POST.		
Quiet Boot	Disabled	Enable this option to hide BIOS Post		
	Enabled	messages during POST.		
Add On ROM Display	Force BIOS	Allows user to force BIOS/Option ROM		
Mode	Keep Current	quiet boot.		
Boot up Number-Lock	On	Choose status of keyboard NUM LOCK		
Boot up Humber Look	Off	key.		
	Enabled	Allows user to choose status of PS/2		
PS/2 Mouse Support	Disabled	mouse support.		
	Auto			
Wait for 'E1' If Error	Enabled	Allows user to disable the "Press F1 to		
	Disabled	detected.		
Hit 'DEL' Message Display	Enabled	Allows user to disable the "Press DEL		
The DEE message Display	Disabled	to enter setup" message during POST.		
Interrupt 10 Centure	Disabled	Allows devices (such as network card)		
interrupt 19 Capture	Enabled	to capture INT19 for booting.		
Drimon Disalau Davias	Auto	Display Davies is used by DIOC DOCT		
Primary Display Device	Onboard VGA	Display Device is used by BIOS POST.		

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3.6.2 – Boot Device Priority Sub-Menu

Use this screen to select options for the Boot Device Priority. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option.

		BIOS	Setup Ut	ility		
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Main Boot Device 1st Boot Dev 2nd Boot De 3rd Boot Dev 4th Boot Dev	Advanced	PCI/PnP [1st [IBA [IBA	Boot FLOPPY I FE Slot 0 GE Slot 0	Security DRIVE] 108 V4110] 0409 V1211] 0408 V1211]	Chipset Use [ENT or [SHIFT select a fi Use [+] or configure time. ← → Sele ↑ ↓ Sele +/- Cha F1 Ger	Exit ER], [TAB] -TAB] to eld r [-] to system ect Screen ect Item ange Option heral Help
					ESC Exit	e and Exit

Feature	Option	Description
Boot Device Priority		
	1st FLOPPY DRIVE	
1st Boot Device	Disabled	Settings for boot priority.
	IBA FE Slot 0108 V4110	These can be customized
	IBA GE Slot 0409 V1211	depending on your preference.
	IBA GE Slot 0408 V1211	
	1st FLOPPY DRIVE	
	Disabled	
2nd Boot Device	IBA FE Slot 0108 V4110	
	IBA GE Slot 0409 V1211	
	IBA GE Slot 0408 V1211	
	1st FLOPPY DRIVE	
	Disabled	
3rd Boot Device	IBA FE Slot 0108 V4110	
	IBA GE Slot 0409 V1211	
	IBA GE Slot 0408 V1211	
	1st FLOPPY DRIVE	
	Disabled	
4th Boot Device	IBA FE Slot 0108 V4110	
	IBA GE Slot 0409 V1211	
	IBA GE Slot 0408 V1211	

3.6.3 – Hard Disk Drives Sub-Menu

Use this screen to select options for the Hard Disk Drives. Use the up and down arrow (\uparrow/\downarrow) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option.

	BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset Exit		
Hard Disk D	Drives				Use [ENTER], [TAB]		
1st Drive		[xx,xxx-xxxxx:xxx]			or [SHIFT_TAB] to select a field		
					Use [+] or [-] to configure system time.		
					←→ Select Screen ↑↓ Select Item +/- Change Option F1 General Help F10 Save and Exit ESC Exit		

Feature	Option	Description
Hard Disk Drives		
1st Drive	xx,xxx-xxxxx:xxx	
	Disabled	

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3.6.4 – Removable Drives Sub-Menu

Use this screen to select options for the Removable Drives. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option.

	BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit	
Removable	Drives	Use [ENTER], [TAB]					
1st Device		[1st F	LOPPY	DRIVE]	or [SHIFT_TAB] to select a field		
					Use [+] or [configure s	-] to ystem time.	
					$\leftarrow \rightarrow$ Select $\uparrow \downarrow$ Select +/- Chan F1 Gene F10 Save ESC Exit	t Screen Item ge Option ral Help and Exit	

Feature	Option	Description		
Removable Drives				
1st Device	1st FLOPPY DRIVE	Specifies the boot sequence for removable drive booting.		
TSt Device	Disabled	This option will show all removable devices.		

3.6.5 – ATAPI CDROM Drives Sub-Menu

Use this screen to select options for the ATAPI CDROM Drives. Use the up and down arrow (Λ/Ψ) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option.

	BIOS Setup Utility								
Main	Advanced	PCI/PnP	Boot	Security	Chipset Exit				
ATAPI CDR	OM Drives				Use [ENTER], [TAB]]			
1st Drive		[xx,xxx	-xxxxx:xx	x]	or [SHIFT_TAB] to select a field				
					Use [+] or [-] to configure system time.				
					$\leftarrow \rightarrow$ Select Screen $\uparrow \downarrow$ Select Item +/- Change Option F1 General Help F10 Save and Exit ESC Exit	n			

Feature	Option	Description
ATAPI CDROM Drives		
1st Drive	xx,xxx-xxxxx:xxx	
I OL DING	Disabled	

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3.7 – BIOS Security Menu

The system can be configured so that all users must enter a password every time the system boots or when BIOS Setup is entered, using either the Supervisor password or User password. The Supervisor and User passwords activate two different levels of password security. If you select password support, you are prompted for a one to six character password. Type the password on the keyboard. The password does not appear on the screen when typed. Make sure you write it down. If you forget it, you must clear CMOS and reconfigure.

	BIOS Setup Utility							
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit		
Security Se	ettings				Use [ENTE	R], [TAB]		
Supervisor F User Passw Change Sup Change Use Clear User F Boot Sector	Password: ord: pervisor Password Password Virus Protecti	vord		[Disabled]	or [SHIFT_ select a fiel Use [+] or [configure s ← → Select +/- Chan F1 Gene F10 Save ESC Exit	-] to ystem time. et Screen t Item ge Option ral Help and Exit		

Feature	Option	Description		
Security Settings				
Supervisor Decoword:	Not Installed	If the password has been set,		
Supervisor Passworu.	Installed	is set, Not Installed displays.		
Lleor Password:	Not Installed	If the password has been set,		
User Fassword.	Installed	is set, Not Installed displays.		
Change Supervisor Password		Select this option to change Supervisor Password.		
Change User Password		Select this option to change User Password.		
Clear User Password		Select this option to clear User Password.		
Boot Sector Virus Protection	Disabled	Protects the first sector of the		
	Enabled	Hard Drive from being written.		

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3.8 - BIOS Chipset Settings Menu

This menu allows the user to customize functions of the Intel Chipsets. Select a menu by highlighting it using the Arrow (\uparrow/\downarrow) keys and pressing Enter. The settings are described on the following pages.

	BIOS Setup Utility						
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit	
Advanced	Chipset Settii	ngs			Use [ENTEF	R], [TAB] or	
WARNING:	Setting wrong may cause sys	values in be stem to malfu	elow sect inction.	ions	[SHIFT_TAE a field	3] to select	
 South Br Intel PCI 	idge Configura -64 Hub 2 Cor	ation			Use [+] or [-] configure sy	l to stem time.	
Clock Gen.	Spread Spect	rum	[Disable	d]	← → Select ↑ ↓ Select +/- Chang Enter Go to F1 Gener F10 Save a ESC Exit	Screen Item Je Option Sub Screen al Help and Exit	

Feature	Option	escription
Advanced Chipset Setting		
Clock Gen. Spread	Disabled	Enabled/Disabled clock generator
Spectrum	Enabled	spread spectrum feature

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3.8.1 – North Bridge Chipset Configuration Sub-Menu

This menu gives options for North Bridge Chipset Configuration settings. Select a menu by highlighting it using the Arrow (\uparrow/\downarrow) keys and pressing Enter. The settings are described on the following pages.

	BIOS Setup Utility							
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit		
Configure a	advanced set	Use [ENTER], [TAB] or						
Chipset Mer	nory Remap F	[SHIFT_TAE a field	3] to select					
CAS Latenc	У		[Auto]		Use [+] or [-] configure sy	to stem time.		
					← → Select ↑ ↓ Select Enter Go to F1 Gener F10 Save a ESC Exit	Screen Item Sub Screen al Help and Exit		

Feature	Option	escription		
Configure advanced setti	ngs for North Brid	dge		
Chipset Memory Remap Feature	Disabled	ENABLE: Allow remapping of overlapped PCI memory above the total physical memory. DISABLE: Do not allow remapping of memory.		
	Enabled			
	Auto	CAS Latency Select Choose 2, 2,5		
CAS Latency	2	or Auto by SPD		
	2.5			



3.8.2 – South Bridge Chipset Configuration Sub-Menu

This menu gives options for South Bridge Chipset Configuration settings. Use the up and down arrow (\uparrow/\downarrow) keys to select an item. Use the Plus and Minus (+/-) keys to change the value of the selected option.

			BIOS Se	tup Utility				
Main	Advanced	PCI/PnP	Boot	Security	Ch	ipset	Power	Exit
South	Bridge Chips	Use [ENTER], [TAB]						
MPS R	levision			[1.1]		or [SH select	to	
						Use [config	+] or [-] to gure system	n time.
					← → ↑ ↓ 5 +/- F1 F10 ESC	Select Scro Select Item Change O General He Save and I Exit	een ption elp Ξxit	

Feature	Option	Description				
South Bridge Chipset Configuration						
MPS Revision	1.1					
	1.4					

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3.8.3 – Intel PCI-64 Hub 2 Configuration Sub-Menu

This menu gives options for Intel PCI-64 Hub 2 Configuration settings. Select a menu by highlighting it using the Arrow (\uparrow/\downarrow) keys and pressing Enter. The settings are described on the following pages.

	BIOS Setup Utility								
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit			
Intel PCI-64	Hub 2 Confi		Use [ENTER], [TAB] or						
PCI Bus Fre	equency		[Auto]		[SHIFT_TAE a field	3] to select			
					Use [+] or [-] configure sy	to stem time.			
					← → Select ↑ ↓ Select Enter Go to F1 Genera F10 Save a ESC Exit	Screen Item Sub Screen al Help and Exit			

Feature	Option	escription
Configure advanced setti	ngs for North Bri	dge
	Auto	This question will allow to select the
	33MHz	maximum PCI bus speed to be
PCI Bus Frequency	66MHz	set to AUTO where hus speed will
	100MHz	be decided based on the capabilities
	133MHz	of the device on that bus.



3.9 – BIOS Exit Menu

You can display an Exit BIOS Setup option by highlighting it Arrow $({\uparrow}/{\downarrow})$ keys and pressing Enter.

		BIOS	Setup U	Itility		
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Exit
Exit Option	s				Use [ENTER]	, [TAB] or
Save Chang	ges and Exit				[SHIFT_TAB] field	to select a
Discard Cha	anges and Exil arges				Use [+] or [-] t system time.	to configure
Load Optima	al Defaults				-	
Load Failsa	fe Defaults				$\leftarrow \rightarrow \text{Select S}$	Screen
					↑ ↓ Select It	em
					Enter Go to S	ub Screen
					F1 Genera	l Help
					F10 Save ar	nd Exit
					ESC Exit	

Save Changes and Exit

Use this option to exit setup utility and re-boot. All new selections you have made are stored into CMOS. System will use the new settings to boot up.

Discard Changes and Exit

Use this option to exit setup utility and re-boot. All new selections you have made are not stored into CMOS. System will use the old settings to boot up.

Discard Changes

Use this option to restore all new setup values that you have made but not saved into CMOS.

Load Optimal Defaults

Use this option to load default performance setup values. Use this option when system CMOS values have been corrupted or modified incorrectly.

Load Failsafe Defaults

Use this option to load all default failsafe setup values. Use this option when troubleshooting

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Chapter 4: SATA/RAID Setup (Option)

The motherboard supports RAID 0 or 1 for Serial ATA drives through the Intel[®] ICH5-R chipset. The Serial ATA as RAID option must be enabled in the BIOS before the system can load the Adaptec[®] RAID option ROM code for Intel[®] RAID. The Adaptec[®] RAID option ROM is a standard PnP (Plug and Play) option ROM that provides a pre-operating system user interface for the Intel[®] RAID implementation. It also allows the boot order to be selected from within the BIOS setup utility.

For this information, please check Tyan's web site at: www.tyan.com

Warning



Before installing the driver into an existing system, backup any important or useful data. Failure to follow this accepted PC practice could result in data loss.

The Intel RAID feature is available in Win2000 and WinXP.

4.1 BIOS Configuration

- Enter the BIOS setup program by pressing the key after the Power-On Self Test (POST) memory test begins.
- Select the Integrated Peripherals menu, then the On Chip IDE Configuration menu.
- Switch the SATA Mode option from IDE to RAID
- Switch the Configure S-ATA as RAID option from [No] to [Yes]
- Press <F10> to save the BIOS setting and exit the BIOS setup program.

4.2 Installing Serial ATA (SATA) hard disks

Installing Serial ATA (SATA) hard disks requires the use of a new SATA data cable (4conductor) which supports the Serial ATA protocol and a SATA power cable. Either end of the SATA data cable can be connected to the SATA hard disk or the SATA connector on the motherboard.



Note

 Both the data and power SATA cables are new cables. You cannot use older 40-pin 80-conductor IDE or regular IDE power cables with SATA hard drives.

Carefully follow any technical instructions that come from the hard disk manufacturer.

Follow the given steps for correct cable installation:

- 1. Attach either cable end to the SATA connector on the motherboard.
- 2. Attach the other cable end to the SATA hard disk.

4.3 Adaptec RAID Configuration Utility

The Serial ATA RAID sets must be configured in the RAID Configuration utility. This configuration can be done by the Adaptec[®] RAID Option ROM. During the Power-On Self Test (POST), the following message will appear for a few seconds: Press **<Ctrl-A>** to enter RAID Configuration Utility. After this message appears, press the **<Ctrl>** and **<A>** keys simultaneously, the following screen menu appears.



Note
The 'Drive Model', 'Serial #', and 'Size' listed in your system can
differ from the following example.



4.4 Manage Array

Use the Manage Arrays option to view array properties and members, and delete arrays. The following sections describe these operations in greater detail.



Display array properties and members

- 1. From the Adaptec Embedded SATA HostRAID Controller#0 Array Configuration Utility menu, select Manage Array.
- 2. From the List of Arrays dialog box, select the array you want to view and press Enter.

The **Array Properties** dialog box appears, showing detailed information on the array. The physical disks associated with the array are displayed here.

3. Press **Esc** to return to the previous menu.

Deleting Arrays



Warning

Take caution in using this option; All data on the RAID drives will be lost! Deleted arrays cannot be restored.

- 1. From the Adaptec Embedded SATA HostRAID Controller#0 Array Configuration Utility menu, select Manage Array.
- 2. Select the array you wish to delete and press Delete.
- 3. In the **Array Properties** dialog box, select Delete and press **Enter**. The following prompt is displayed:

WARNING: Deleting the array will result in data loss! Do you want to delete the array? (Yes/No):

- 4. Press Yes to delete the array or No to return to the previous menu.
- 5. Press **Esc** to return to the previous menu.

4.5 Create Array

Before creating arrays, make sure the disks for the array are connected and installed in your system. Note that disks with no usable space, or disks that are uninitialized are shown in gray and cannot be used.





Follow the given steps to create a RAID 0 or RAID 1 Volume

- 1. From the Adaptec Embedded SATA HostRAID Controller#0 Array Configuration Utility menu, select option Create Array and press the <Enter> key.
- 2. Select the disks for the new array and press <Ins> key. To deselect any disk, highlight the disk and press **Delete**.
- 3. Press Enter when both disks for the new array are selected. The **Array Properties** menu displays.

To assign properties to the new array:

- 1. In the Array Properties menu, select an array type and press Enter. Note that only the available array types, RAID 0 and RAID 1, are displayed. Each of these types requires two drives.
- 2. Type in an optional label for the array and press Enter.
- 3. For RAID 0, select the desired stripe size. Available stripe sizes are 16KB, 32KB and 64KB (default)
- 4. Create RAID via allows you to select between the different creation methods for RAID 0 and RAID 1. The following table gives examples of when each is appropriate.

RAID level	Create RAID via	When appropriate
RAID 0	No Init	Creating a RAID 0 on new drives.
RAID 0	Migrate*	Creating a RAID 0 from one new drive and one drive with data you wish to preserve.
RAID 1	Build*	Any time you wish to create a RAID 1, but especially if you have data on one drive that you wish to preserve.
RAID 1	Clear	Creating a RAID 1 on new drives, or when you want to ensure that the array contains no data after creation.
RAID 1	Quick Init	Fastest way to create a RAID 1. Appropriate when using new drives.
* If you select source drive. on the new drive and the new drite and the new drite a	t Migrate for RAID 0, The contents of the rive will be lost.	or Build for RAID 1, you will be asked to select the source drive will be preserved. However, the data

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Before adding a new drive to an array, back up any data contained on the new drive. Otherwise, all data will be lost.



A RAID 1 created using the Quick Init option may return some data miscompares if you later run a consistency check. This is normal and is not a cause for concern.

The Adaptec RAID Configuration Utility allows you to use drives of different sizes in a RAID 1. If you choose the smaller drive as the second drive, you will be warned about the risk of data loss.



Warning

Do not interrupt the creation of a RAID 0 using the Migrate option. If you do, there is no way to restart and no way to recover the data that was on the source drive.

5. When you are finished, press **Done**.

4.6 Add/Delete Hotspare

===Adaptec Embedded SATA HostRAID Controller #0 Array onfiguration Utility===
Main Menu Manage Array Create Array Add/Delete Hotspare Initialize Drives
Display, Add, Delete hotspare

Create a drive spare:

1. Select option Add / Delete Hotspare and press the <Enter> key to create the drive spare.

The following prompt is displayed: Do you want to create a spare? (Yes/No):

- 2. Press Y to create the drive spare.
- 3. Press Esc to return to the previous menu.



Delete a drive spare:

- Select option Add / Delete Hotspare and press the <Enter> key to delete the drive 4. spare. The following prompt is displayed:
 - Do you want to delete the spare? (Yes/No):
- 5. Press Y to delete the drive spare.
- 6. Press Esc to return to the previous menu.

4.7 Initialize Drives

If an installed disk does not appear in the disk selection list for creating a new array or if it appears grayed out, you may have to initialize it before you can use it as part of an array. Drives attached to the controller must be initialized before they can be used in an array.

===Adaptec Embedded SATA HostRAID Controller #0 Array	onfiguration Utility===
Main Menu Manage Array Create Array Add/Delete Hotspare Initialize Drives	
Prepare drives for creating arrays.	



Warning

Take caution in using this option; Initialization will erase all Array information from the selected drives. Any away using any of these drives as members will be affected.

- 1. Select Initialize Drives.
- Use the < \uparrow > and < \downarrow > keys to highlight the disk you wish to Initialize and press **Insert**.
- 2. 3. Repeat step 2 so that both drives to be initialized are selected.
- 4. Press <Enter>
- Read the warning message and ensure that you have selected the correct disk drives to initialize. Type \bf{Y} to continue. 5.

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4.8 Disk Utilities

The Disk Utilities enable you to low-level format or verify the media of your Serial ATA hard disks.

From the Adaptec RAID Configuration Utility menu, select Disk Utilities.



1. Press the <Enter> key to select SATA Disk.

Adaptec Embedded SATA HostRAID Controller#0 Select SATA Disk and press <enter> SATA Port #0 Maxtor 6Y120M0 YAR51BW0 SATA Port #1 Maxtor 6Y120M0 YAR51BW0</enter>
Select SATA Disk and press <enter> SATA Port #0 Maxtor 6Y120M0 YAR51BW0 SATA Port #1 Maxtor 6Y120M0 YAR51BW0</enter>
SATA Port #0 Maxtor 6Y120M0 YAR51BW0 SATA Port #1 Maxtor 6Y120M0 YAR51BW0
SATA Port #2 No device SATA Port #3 No device SATA Port #4 No device SATA Port #5 No device SATA Port #6 No device
Only drives present at POST are display d

Arrow keys to move cursor, <Enter> to select option, <Esc> to _xit (*=default)



Take caution in using this option;

Format Disk-This drive is about to be formatted. All data on the disk will be erased.

Warning

Verify Disk Media- This drive will be scanned for media defects. All recoverable defects will be remapped.



2. Select options Format Disk or Verify Disk Media and press the <Enter> key.

You are offered the following options:

Format Disk— Simulates a format of the hard drive by writing zeros to the entire disk.

Verify Disk Media— Scans the media of a disk drive for defects. Any errors found are corrected.

3. Press **Esc** to return to the previous menu.




Chapter 5: Diagnostics

5.1 – Hardware Monitor Information

ICH5 SMBus base address = EC80h

Winbond83782D hardware Monitor Information

Hardware Monitor Chip 1: Slave Address: Chip Clock:			Winbond83782D 0x29h 14.318MHz		
Sensor Name	<u>Bank</u>	<u>Register</u>	<u>Other</u>		
CPU1 Fan CPU2 Fan Chassis Fan 3 System Volt 2 2V	0 0 0	0x28h 0x29h 0x2Ah 0x22h	controlled by PWM1 controlled by PWM3 controlled by PWM4		
System Volt 3.3V System Volt 5V System Volt 12V Battery Voltage	0 0 5	0x23h 0x24h 0x51h	R1 = 28K, R2 = 10K		
System(VRM) temp CPU1 533FSB tem CPU2 533FSB tem	0 p 1 p 2	0x27h 0x50h 0x50h	2N3904 diode PII compatible diode PII compatible diode		

W83627HF Super IO hardware Monitor Information

Hardware Monito Slave Address: Chip Clock:	or Chip 2:	WinbondW83627HF (Super IO) 0x2ah 48.000MHz
Sensor Name	Bank	<u>Register</u>
Chassis Fan 4 Chassis Fan 5 Chassis Fan 6	0 0 0	0x28h 0x29h 0x2Ah

On chip Xeon Thermal Sensor for 400Mhz FSB Xeon

Hardware Monitor Chip 3: Slave Address:		Intel Xeon Thermal Sensor 0x18h	
Sensor Name	<u>Bank</u>	<u>Register</u>	
CPU1 temperature	0	0x01h	

On chip Xeon Thermal Sensor for 400Mhz FSB Xeon

Hardware Monitor Chip 4:		Intel Xeon Thermal Sensor	
Slave Address:		0x19h	
<u>Sensor Name</u>	<u>Bank</u>	<u>Register</u>	
CPU2 temperature	0	0x01h	

Programming CPU thermal sensors for custom Hardware Monitoring requirements (the GPIO pins are on ICH5)

GPIO27	GPIO28	CHANNEL#	DEVICES
Х	Х	Х	Winbond 83782D (Always on)
1	1	3	DIMM SPD (6 devices), and CK408B clock chip
-			
0	1	2	CPU1 and CPU2 Thermal sensor, Super IO W83627HF
0 1	1 0	2 1	CPU1 and CPU2 Thermal sensor, Super IO W83627HF Reserved

Note: if you experience problems with setting up your system, always check the following things in the following order:

Memory, Video, CPU

By checking these items, you will most likely find out what the problem might have been when setting up your system. For more information on troubleshooting, check the Tyan website at: http://www.tyan.com.

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http://www.TYAN.com	

5.2 Beep Codes

Fatal errors, which halt the boot process, are communicated through a series of audible beeps. For example, if the BIOS POST can initialize the video but an error occurs, an error message will be displayed. If it cannot display the message, it will report the error as a series of beeps.

Beep codes that are used by AMIBIOS:

Number of Beeps	Description
1	Memory refresh timer error
2	Parity error
3	Main memory read / write test error
4	Motherboard timer not operational
5	Processor error
6	Keyboard controller BAT test error / No memory inserted
7	General exception error / Non Reg / ECC DIMMs inserted
8	Display memory error
9	ROM checksum error
10	CMOS shutdown register read/write error
11	Cache memory bad

Troubleshooting BIOS Beep Codes

Number of Beeps	Trouble shooting action
1, 2 or 3	Reseat the memory, or replace with known good modules. Make sure they are Registered ECC.
4-7, 9-11	 Fatal error indicating a serious problem with the system. Consult your system manufacturer. Before declaring the motherboard beyond all hope, eliminate the possibility of interference by a malfunctioning add-in card. Remove all expansion cards except the video adapter. If the beep codes are generated even when all other expansion cards are absent, the motherboard has a serious problem. Consult your system manufacturer. If the beep codes are not generated when all other expansion cards are absent, one of the add-in cards is causing the malfunction. Insert the cards back into the system one at a time until the problem happens again. This will reveal the malfunctioning add-in card.
8	If the system video adapter is an add-in card, replace or reseat the video adapter. If the video adapter is an integrated part of the system board, the board may be faulty.

Before contacting your vendor or Tyan Technical Support, be sure that you note as much as you can about the beep code length and order that you experience. Also, be ready with information regarding add-in cards, drives and O/S to speed the support process and come to a quicker solution.



5.3 Flash Utility

Every BIOS file is unique for the motherboard it was designed for. For Flash Utilities, BIOS downloads, and information on how to properly use the Flash Utility with your motherboard, please check the Tyan web site: <u>http://www.tyan.com/</u>



Please be aware that by flashing your BIOS, you agree that in the event of a BIOS flash failure, you must contact your dealer for a replacement BIOS. There are no exceptions. Tyan does not have a policy for replacing BIOS chips directly with end users. In no event will Tyan be held responsible for damages done by the end user.



Appendix I: Glossary

ACPI (Advanced Configuration and Power Interface): a power management specification that allows the operating system to control the amount of power distributed to the computer's devices. Devices not in use can be turned off, reducing unnecessary power expenditure.

AGP (Accelerated Graphics Port): a PCI-based interface which was designed specifically for demands of 3D graphics applications. The 32-bit AGP channel directly links the graphics controller to the main memory. While the channel runs at only 66 MHz, it supports data transmission during both the rising and falling ends of the clock cycle, yielding an effective speed of 133 MHz.

ATAPI (AT Attachment Packet Interface): also known as IDE or ATA; a drive implementation that includes the disk controller on the device itself. It allows CD-ROMs and tape drives to be configured as master or slave devices, just like HDDs.

ATX: the form factor designed to replace the AT form factor. It improves on the AT design by rotating the board 90 degrees, so that the IDE connectors are closer to the drive bays, and the CPU is closer to the power supply and cooling fan. The keyboard, mouse, USB, serial, and parallel ports are built-in.

Bandwidth: refers to carrying capacity. The greater the bandwidth, the more data the bus, phone line, or other electrical path, can carry. Greater bandwidth, then, also results in greater speed.

BBS (BIOS Boot Specification): is a feature within the BIOS that creates, prioritizes, and maintains a list of all Initial Program Load (IPL) devices, and then stores that list in NVRAM. IPL devices have the ability to load and execute an OS, as well as provide the ability to return to the BIOS if the OS load process fails for some reason. At that point, the next IPL device is called upon to attempt loading of the OS.

BIOS (Basic Input/Output System): the program that resides in the ROM chip, and provides the basic instructions for controlling your computer's hardware. Both the operating system and application software use BIOS routines to ensure compatibility.

Buffer: a portion of RAM, which is used to temporarily store data, usually from an application, though it is also used when printing, and in most keyboard drivers. The CPU can manipulate data in a buffer before copying it, all at once, to a disk drive. While this improves system performance --- reading to or writing from a disk drive a single time is much faster than doing so repeatedly --- there is also the possibility of losing your data should the system crash. Information stored in a buffer is temporarily stored, not permanently saved.

Bus: a data pathway. The term is used especially to refer to the connection between the processor and system memory, and between the processor and PCI or ISA local buses.

Bus mastering: allows peripheral devices and IDEs to access the system memory without going through the CPU (similar to DMA channels).

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Cache: a temporary storage area for data that will be needed often by an application. Using a cache lowers data access times, since the needed information is stored in the SRAM instead of in the slow DRAM. Note that the cache is also much smaller than your regular memory: a typical cache size is 512KB, while you may have as much as 4GB of regular memory.

Cache size: refers to the physical size of the cache onboard. This should not be confused with the cacheable area, which is the total amount of memory, which can be scanned by the system in search of data to put into the cache. A typical setup would be a cache size of 512KB, and a cacheable area of 512MB. In this case, up to 512KB of the main memory onboard is capable of being cached. However, only 512KB of this memory will be in the cache at any given moment. Any main memory above 512MB could never be cached.

Closed and open jumpers: jumpers and jumper pins are active when they are "on" or "closed", and inactive when they are "off" or "open".

CMOS (Complementary Metal-Oxide Semiconductors): chips that hold the basic startup information for the BIOS.

COM port: another name for the serial port, which is called as such because it transmits the eight bits of a byte of data along one wire, and receives data on another single wire (that is, the data is transmitted in serial form, one bit after another). Parallel ports transmit the bits of a byte on eight different wires at the same time (that is, in parallel form, eight bits at the same time).

DDR (Double Data Rate): is a technology designed to double the clock speed of the memory. It activates output on both the rising and falling edge of the system clock rather than on just the rising edge, potentially doubling output.

DIMM (Dual In-line Memory Module): faster and more capacious form of RAM than SIMMs, and do not need to be installed in pairs.

DIMM bank: sometimes called DIMM sockets, because the physical slot and the logical unit are the same. That is, one DIMM module fits into one DIMM socket, which is capable of acting as a memory bank.

DMA (Direct Memory Access): channels that are similar to IRQs. DMA channels allow hardware devices (like soundcards or keyboards) to access the main memory without involving the CPU. This frees up CPU resources for other tasks. As with IRQs, it is vital that you do not double up devices on a single line. Plug-n-Play devices will take care of this for you.

Doze mode: in this mode, only the CPU's speed is slowed. DRAM (Dynamic RAM): widely available, very affordable form of RAM, which has the unfortunate tendency to lose data if it is not recharged regularly (every few milliseconds). This refresh requirement makes DRAM three to ten times slower than non-recharged RAM such as SRAM.

ECC (Error Correction Code or Error Checking and Correcting): allows data to be checked for errors during run-time. Errors can subsequently be corrected at the same time that they're found.

EEPROM (Electrically Erasable Programmable ROM): also called Flash BIOS, is a ROM chip, which can be updated. This allows you to keep up with changes in the BIOS programs without having to buy a new chip. TYAN's BIOS updates can be found at http://www.TYAN.com

EMRL: Embedded RAID Logic. An Adaptec specific RAID technology.

ESCD (Extended System Configuration Data): a format for storing information about Plug-n-Play devices in the system BIOS. This information helps properly configure the system each time it boots.

Fault-tolerance: a term describing a system where one component can quickly be replaced without causing a loss of service, such as in a RAID system.

Firmware: low-level software that controls the system hardware. **Form factor:** an industry term for the size, shape, power supply type, and external connector type of the Personal Computer Board (PCB) or motherboard. The standard form factors are the AT and ATX, although TYAN also makes some Baby-AT and ATX Footprint boards.

Global timer: onboard hardware timer, such as the Real-Time Clock (RTC).

Handshaking: a form of encryption. One system, typically the server, sends an encryption scheme to another agent, typically a client. Thus, the client's data is protected during transmittal to the server.

HDD: stands for Hard Disk Drive, a type of fixed drive.

H-SYNC: controls the horizontal synchronization/properties of the monitor.

IC (Integrated Circuit): the formal name for the computer chip.

IDE (Integrated Device/Drive Electronics): a simple, self-contained HDD interface. It can handle drives up to 8.4 GB in size. Almost all IDEs sold now are in fact Enhanced IDEs (EIDEs), with maximum capacity determined by the hardware controller.

IDE INT (IDE Interrupt): a hardware interrupt signal that goes to the IDE.

I/O (Input/Output): the connection between your computer and another piece of hardware (mouse, keyboard, etc.)

Initial Program Load (IPL): a feature built into BBS-compliant devices, describing those devices as capable of loading and executing an OS, as well as being able to provide control back to the BIOS if the loading attempt fails.

IPL: see Initial Program Load.

IPMB: Intelligent Platform Management Bus. It is a subset of I2C or SMBus. Brief description, it is used to send IPMI messages/commands. (The communication uses standard I2C or SMBus protocol. It is just that the message being sent into the bus is formatted to IPMI standard.)

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IRQ (Interrupt Request): an electronic request that runs from a hardware device to the CPU. The interrupt controller assigns priorities to incoming requests and delivers them to the CPU. It is important that there is only one device hooked up to each IRQ line; doubling up devices on IRQ lines can lock up your system. Plug-n-Play operating systems can take care of these details for you.

ISA (Industry Standard Architecture): a slower 8- or 16-bit bus (data pathway).

Latency: the amount of time that one part of a system spends waiting for another part to catch up. This is most common when the system sends data out to a peripheral device, and it waiting for the peripheral to send some data back (peripherals tend to be slower than onboard system components).

Mirroring: see RAID.

NVRAM: ROM and EEPROM are both examples of Non-Volatile RAM, memory that holds its data without power. DRAM, in contrast, is volatile.

OEMs (Original Equipment Manufacturers): Compaq or IBM package other companies' motherboards and hardware inside their case and sell them.

Parallel port: transmits the bits of a byte on eight different wires at the same time (that is, in parallel form, eight bits at the same time).

PCI (Peripheral Component Interconnect): a 32 or 64-bit local bus (data pathway), which is faster than the ISA bus. Local buses are those, which operate within a single system (as opposed to a network bus, which connects multiple systems).

PCI PIO (PCI Programmable Input/Output) modes: the data transfer modes used by IDE drives. These modes use the CPU for data transfer (in contrast, DMA channels do not). PCI refers to the type of bus used by these modes to communicate with the CPU. PCI-to-PCI bridge: allows you to connect multiple PCI devices onto one PCI slot.

Pipeline burst SRAM: a fast secondary cache. It is used as a secondary cache because SRAM is slower than SDRAM, but usually larger. Data is cached first to the faster primary cache, and then, when the primary cache is full, to the slower secondary cache.

Pipelining: improves system performance by allowing the CPU to begin executing a second instruction before the first is completed. A pipeline can be likened to an assembly line, with a given part of the pipeline repeatedly executing a set part of an operation on a series of instructions.

PM timers (Power Management timers): software timers that count down the number of seconds or minutes until the system times out and enters sleep, suspend, or doze mode.

PnP (Plug-n-Play): a design standard that has become ascendant in the industry. Plug-n-Play devices require little set-up to use. Novice end users can simply plug them into a computer that is running on a Plug-n-Play aware operating system (such as Windows 98), and go to work. Devices and operating systems that are not Plug-n-Play require you to reconfigure your system each time you add or change any part of your hardware.

PXE (Pre-boot Execution Environment): one of four components that together make up the Wired for Management 2.0 baseline specification. PXE was designed to define a standard set of preboot protocol services within a client, towards the goal of allowing networked-based booting to boot using industry standard protocols.

RAID (Redundant Array of Independent Disks): a way for the same data to be stored in different places on many hard drives. By using this method, the data is stored redundantly, also the multiple hard drives will appear as a single drive to the operating system. RAID level 0 is known as striping, where data is striped (or overlapped) across multiple hard drives, but offers no fault-tolerance. RAID level 1 is known as mirroring, which stores the data within at least two hard drives, but does not stripe. RAID level 1 also allows for faster access time and fault-tolerance, since either hard drive can be read at the same time. RAID level 0+1 is both striping and mirroring, providing fault-tolerance, striping, and faster access all at the same time.

RAIDIOS: RAID I/O Steering (Intel)

RAM (Random Access Memory): technically refers to a type of memory where any byte can be accessed without touching the adjacent data, is often used to refer to the system's main memory. This memory is available to any program running on the computer.

ROM (Read-Only Memory): a storage chip which contains the BIOS; the basic instructions required to boot the computer and start up the operating system.

SCSI Interrupt Steering Logic (SISL): Architecture that allows a RAID controller, such as AcceleRAID 150, 200 or 250, to implement RAID on a system board-embedded SCSI bus or a set of SCSI busses. SISL: SCSI Interrupt Steering Logic (LSI) (only on LSI SCSI boards)

SDRAM (Synchronous Dynamic RAM): called as such because it can keep two sets of memory addresses open simultaneously. By transferring data alternately from one set of addresses and then the other, SDRAM cuts down on the delays associated with non-synchronous RAM, which must close one address bank before opening the next.

SRAM (Static RAM): unlike DRAM, this type of RAM does not need to be refreshed in order to prevent data loss. Thus, it is faster and more expensive.

Serial ATA: Often abbreviated *SATA* or *S-ATA*, an evolution of the Parallel ATA physical storage interface. Serial ATA is a serial link -- a single cable with a minimum of four wires creates a point-to-point connection between devices. Transfer rates for Serial ATA begin at 150MBps. One of the main design advantages of Serial ATA is that the thinner serial cables facilitate more efficient airflow inside a form factor and also allow for smaller chassis designs. In contrast, IDE cables used in parallel ATA systems are bulkier than Serial ATA cables and can only extend to 40cm long, while Serial ATA cables can extend up to one meter.

Serial port: Called such because it transmits the eight bits of a byte of data along one wire, and receives data on another single wire (the data is transmitted in serial form, one bit after another).

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SIMM (Single In-line Memory Module): Formerly the most common RAM for motherboards. They must be installed in pairs, and don't have the capacity or the speed of DIMM modules.

Sleep/Suspend mode: in this mode, all devices except the CPU shut down.

SSI (Server System Infrastructure): an industry initiative intended to provide ready-touse design specifications for common server hardware elements (chassis, power supplies, and racks) to promote and support server industry growth.

Standby mode: in this mode, the video and hard drives shut down; all other devices continue to operate normally.

Striping: see RAID

UltraDMA-33/66/100: a fast version of the old DMA channel. UltraDMA is also called UltraATA. Without a proper UltraDMA controller, your system cannot take advantage of higher data transfer rates of the new UltraDMA/UltraATA hard drives.

USB (Universal Serial Bus): a versatile port. This one port type can function as a serial, parallel, mouse, keyboard or joystick port. It is fast enough to support video transfer, and is capable of supporting up to 127 daisy-chained peripheral devices.

VGA (Video Graphics Array): the PC video display standard

V-SYNC: controls the vertical scanning properties of the monitor.

Wake On LAN (WOL): A remote wake-up technology that enables you to remotely power systems "on" for off-hours maintenance. A result of the Intel-IBM Advanced Manageability Alliance and part of the Wired for Management Baseline Specification, this technology helps save time on automated software installations, upgrades, disk backups and virus scans. Equally important, it increases end-user productivity by moving such planned disruptions to off-hours.

Watch Dogtimer (WDT): A device that performs a specific operation after a certain period of time if something goes wrong with an electronic system and the system doesn't recover on its own. The WDT contains a digital counter that counts down to zero at a constant speed from a preset number. The counter speed is kept constant by a clock circuit. If the counter reaches zero before the computer recovers, a signal is sent to designated circuits to perform the desired action, e.g. reset the system. A normally functioning system must re-load the WDT occasionally, to prevent the counter from reaching zero.

ZCR: Zero Channel RAID. PCI card that allows a RAID card to use the onboard SCSI chip, thus lowering cost of RAID solution

ZIF Socket (Zero Insertion Force socket): these sockets make it possible to insert CPUs without damaging the sensitive CPU pins. The CPU is lightly placed in an open ZIF socket, and a lever is pulled down. This shift the processor over and down, guiding into the board and locking it into place.

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Appendix II: SMDC Information

Tyan Server Management Daughter Card (SMDC) is a powerful yet cost-efficient solution for high-end server management hardware packages. Tyan's goal is to provide remote system monitoring and control even when the operating system is absence or simply fails. This empowers Tyan's server board with advanced industrial-standard features.

Tyan SMDC is a snap-in card that provides essential server management solution. It enables any IT Manager by providing multi-interfaces to access the hardware remotely and perform **monitor**, **control** and **diagnose** activities effectively.

Tyan SMDC is powered by an intelligent controller known as Baseboard Management Control (BMC). BMC is a standalone mini-CPU and runs on its own Real Time Operating System (RTOS) to complete all different kinds of tasks. Backed by QLogic's ARM7 technology, IT manager can rest assure his server machines are always taken care.

Tyan SMDC is not a peripheral card. Unlike regular peripheral card such as AGP card, Network card or SCSI card, SMDC does not require any hardware specific driver. As long as a standby power comes into the system, SMDC will begin looking after the system.

Tyan SMDC provides diversified methods to communicate with the hardware. IT manager has the flexibility to choose among *Keyboard Controller Style* (KCS), *Block Transfer* (BT) style, Intelligent Chassis Management Bus (ICMB), Intelligent Platform Management Bus (IPMB), Emergency Management Port (EMP) and standard IPMI-Over-LAN communication as defined in latest IPMI 1.5 specification.

Tyan SMDC is compatible with all IPMI-compliance software as well as Tyan System Operator $^{\rm TM}$ (TSO) software package.

By adding SMDC, Tyan's server board becomes a highly manageable and IPMI compatible system with all the advanced features suggesting in IPMI Spec.

More detailed information on Tyan's SMDC card can be found on our website: <u>Http://www.Tyan.com</u>

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Appendix III: Riser Cards

TY/AN			
	M2033	M2037	M2044
UPC Code	635872-008474	635872-007255	635872-008368
Form Factor	1U	10	2 <mark>U</mark>
Gold Finger Type	Universal	Universal	Universal
Number of Slots	1	1	3
Slot Type	3.3V	5V	3.3V
Slot Speed	133MHz 100MHz 66MHz 33MHz	133MHz 100MHz 66MHz 33MHz	133MHz 100MHz 66MHz
Supported Products	ALL	ALL	S2735 S2726 (R02) S2721-533 (R03) S2725 S2850 S2880

Please check with your distributor for these riser cards or check <u>http://www.etyan.com</u> to purchase the desired card(s).

Appendix IV: SCSI Daughter Card Information

Tyan SCSI daughter card M7902 is a dual-channel 64-bit, 133MHz PCI-X SCSI card that delivers fast, reliable performance for enterprise servers and external storage using Ultra320 SCSI technology.

Tyan SCSI daughter card M7902 offers the following basic and advanced features:

- Up to 320 MByte/sec SCSI data transfer rates
- 133MHz, 64bit, PCI-X interface that provides 1066 MByte/sec bandwidth
- PCI 2.3 and PCI-X 1.0A compliant
- Supports up to 15 Ultra 320 devices per channel on a standard cable configuration up to 12 meters

Please check with your distributor for SCSI daughter card M7902 or check <u>http://www.etyan.com</u> to purchase the desired card(s).

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Technical Support

If a problem arises with your system, you should turn to your dealer for help first. Your system has most likely been configured by them, and they should have the best idea of what hardware and software your system contains. Hence, they should be of the most assistance. Furthermore, if you purchased your system from a dealer near you, you can actually bring your system to them to have it serviced, instead of attempting to do so yourself (which can have expensive consequences).

Help Resources:

- 1. See the beep codes section of this manual on pg 38.
- 2. See the TYAN website for FAQ's, bulletins, driver updates, and
- other information: http://www.TYAN.com
- 3. Contact your dealer for help BEFORE calling TYAN.
- 4. Check the TYAN user group: alt.comp.periphs.mainboard.TYAN

Returning Merchandise for Service

During the warranty period, contact your distributor or system vendor FIRST for any product problems. This warranty only covers normal customer use and does not cover damages incurred during shipping or failure due to the alteration, misuse, abuse, or improper maintenance of products.

NOTE: A receipt or copy of your invoice marked with the date of purchase is required before any warranty service can be rendered. You may obtain service by calling the manufacturer for a Return Merchandise Authorization (RMA) number. The RMA number should be prominently displayed on the outside of the shipping carton and the package should be mailed prepaid. TYAN will pay to have the board shipped back to you.



Notice for the USA Compliance Information Statement (Declaration of Conformity Procedure) DoC FCC Part 15: This device complies with part 15 of the FCC Rules

Operation is subject to the following conditions:

- 1) This device may not cause harmful interference, and
- 2) This device must accept any interference received including interference that may cause undesired operation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try one or more of the following measures:
 - Reorient or relocate the receiving antenna.
 - Increase the separation between the equipment and the receiver.
 - Plug the equipment into an outlet on a circuit different from that of the receiver.
 - Consult the dealer on an experienced radio/television technician for help.

Notice for Canada

This apparatus complies with the Class B limits for radio interference as specified in the Canadian Department of Communications Radio Interference Regulations. (Cet appareil est conforme aux norms de Classe B d'interference radio tel que specifie par le Ministere Canadien des Communications dans les reglements d'ineteference radio.)



Notice for Europe (CE Mark) This product is in conformity with the Council Directive 89/336/EEC, 92/31/EEC (EMC).

CAUTION: Lithium battery included with this board. Do not puncture, mutilate, or dispose of battery in fire. Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by manufacturer. Dispose of used battery according to manufacturer instructions and in accordance with your local regulations.

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